



UNITED STATES PATENT AND TRADEMARK OFFICE

UNITED STATES DEPARTMENT OF COMMERCE
United States Patent and Trademark Office
Address: COMMISSIONER FOR PATENTS
P.O. Box 1450
Alexandria, Virginia 22313-1450
www.uspto.gov

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/757,226	01/14/2004	Raymond J. Blasko	DP-310692	3255

22851 7590 04/01/2005
DELPHI TECHNOLOGIES, INC.
M/C 480-410-202
PO BOX 5052
TROY, MI 48007

EXAMINER

CARPIO, IVAN HERNAN

ART UNIT PAPER NUMBER

2841

DATE MAILED: 04/01/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary	Application No.	Applicant(s)	
	10/757,226	BLASKO ET AL.	
	Examiner	Art Unit	
	Ivan H. Carpio	2841	

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☐ Responsive to communication(s) filed on ____.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-14 is/are pending in the application.
- 4a) Of the above claim(s) ____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) ____ is/are allowed.
- 6) ☒ Claim(s) 1-14 is/are rejected.
- 7) ☐ Claim(s) ____ is/are objected to.
- 8) ☐ Claim(s) ____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on ____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. ____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|---|--|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. ____ |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date ____ | 6) <input type="checkbox"/> Other: ____ |

Claim Objections

Claim 1 is objected to for improper grammatical material, line 4 states "an insulator block mounted on a upper surface..." , should be written as "an insulator block mounted on **an** upper surface..."

Claim Rejections - 35 USC § 102

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

Claims (1-14) rejected under 35 U.S.C. 102(b) as being anticipated by

Grider (U.S. Patent 5,105,262).

With respect to claim 1, Grider teaches an electrical assembly comprising, a lower housing (figure 3, element 12), a circuit board mounted in the lower housing (figure 3, element 11), an insulator block mounted on an upper surface of the circuit board (figure 10, element 30), holding a plurality of conductive terminals (figure 10, element 25), so that the terminals have contact heads extending above a top surface of the insulator block and connector tails extending below a bottom surface of the insulator block and attached to the circuit board a face seal above the insulator block so that the contact heads of the terminals extend through the face seal (figure 10, element 31), an upper housing (figure 3, element 13) having an upstanding shroud (figure 3, element

Art Unit: 2841

26), and means to attach the upper housing so that the contact heads of the terminals are disposed within the shroud (figure 10) and the face seal is compressed between the top surface of the insulator block and a lower surface of the upper housing (figure 10, note element 31).

With respect to claim 2 and in accordance with claim 1, Grider teaches that the shroud has an outer periphery (figures 3 and 10, element 26) and the insulator block has an outer periphery that is smaller than the outer periphery of the shroud spaced inwardly (figure 10, elements 26 and 30).

With respect to claim 3 and in accordance with claim 2, Grider teaches that the smaller outer periphery of the insulator block provides a space beneath the upper housing for attaching electrical and/or electronic components to the circuit board adjacent the insulator block (figure 10), note the space underneath the shroud.

With respect to claim 4 and in accordance with claim 2, Grider teaches the smaller outer periphery of the insulation block is spaced inwardly of the outer periphery of the shroud (figure 10).

With respect to claim 5 and in accordance with claim 3, Grider teaches the smaller outer periphery of the insulation block is spaced inwardly of the outer periphery of the shroud (figure 10).

With respect to claim 6 and in accordance with claim 1, Grider teaches that the means to attach the upper housing includes the upper housing being attached to the insulator block (figure 10).

Art Unit: 2841

With respect to claim 7 and in accordance to claim 1, Grider teaches that the means to attach the upper housing includes the upper housing being attached to the lower housing (figure 6, element 51).

With respect to claim 8, Grider teaches an electrical assembly comprising, a lower housing (figure 3, element 12), a circuit board mounted in the lower housing (figure 3, element 11), an insulator block mounted on an upper surface of the circuit board (figure 10, element 30), holding a plurality of conductive terminals so that the terminals have contact heads extending above a top surface of the insulator block and connector tails extending below a bottom surface of the insulator block and attached to the circuit board (figure 10, element 25), an upper housing (figure 3, element 13) having an upstanding shroud (figure 3, element 26), and means to attach the upper housing so that the contact heads of the terminals are disposed within the shroud (figure 10), and the shroud having an outer periphery and the insulator block having an outer periphery that is smaller than the outer periphery of the shroud (figure 10, elements 26 and 30).

With respect to claim 9 and in accordance with claim 8, Grider teaches that the smaller outer periphery of the insulator block provides a space beneath the upper housing for attaching electrical and/or electronic components to the circuit board adjacent the insulator block (figure 10, note the space underneath the shroud).

Art Unit: 2841

With respect to claim 10 and in accordance with claim 8, Grider teaches the smaller outer periphery of the insulation block is space inwardly of the outer periphery of the shroud (figure 10).

With respect to claim 11 and in accordance with claim 9, Grider teaches the smaller outer periphery of the insulation block is space inwardly of the outer periphery of the shroud (figure 10).

With respect to claim 12 and in accordance with claim 8, Grider teaches that the means to attach the upper housing includes the upper housing being attached to the insulator block (figure 10).

With respect to claim 13 and in accordance to claim 8, Grider teaches that the means to attach the upper housing includes the upper housing being attached to the lower housing (figure 6, element 51).

With respect to claim 14, Grider teaches an electrical assembly comprising, a lower housing (figure 3, element 12), a circuit board mounted in the lower housing (figure 3, element 11), an insulator block mounted on an upper surface of the circuit board (figure 10, element 30), holding a plurality of conductive terminals so that the terminals have contact heads extending above a top surface of the insulator block and connector tails extending below a bottom surface of the insulator block and attached to the circuit board (figure 10, element 25), a face seal above the insulator block so that the contact heads of the terminals extend through the face seal (figure 10, element 31), an upper housing (figure 3, element 13) having an upstanding shroud (figure 3, element 26), the upper housing being attached to the insulator block so that the contact

Art Unit: 2841

heads of the terminals are disposed within the shroud and the face seal is compressed between a top surface of the insulator block and a lower surface of the upper housing (figure 10), the upper housing being attached to the lower housing (figures 5 and 6), and the shroud having an outer periphery (figures 3 and 10, element 26) and the insulator block having outer periphery that is smaller than the outer periphery of shroud (figure 10, element 26 and 30)

Conclusion


The prior art made of record and not relied upon is considered pertinent to applicant's disclosure. Snider U.S. Patents 6606252 B1, 4174175, and Pub. No. US 2003/0161110 A1.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Ivan H. Carpio whose telephone number is 571-272-8396. The examiner can normally be reached on M-R.6:00am - 4:30pm.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Kammie Cuneo can be reached on 571-272-1957. The fax phone number for the organization where this application or proceeding is assigned is 703-872-9306.

Art Unit: 2841

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



KAMAND CUNEO
SUPERVISORY PATENT EXAMINER
TECHNOLOGY CENTER 2800